AMENDMENTS TO THE CLAIMS:

Please cancel claims 13 - 19, without prejudice or disclaimer of their subject matter, and amend claims 4 and 10 as indicated below. This listing of claims will replace all prior versions and listings of claims in the application:

1. (Original) A semiconductor apparatus comprising:

a substrate;

a buffer layer made of a monocrystal semiconductor material and formed on the

substrate;

a strained-Si layer formed on the buffer layer and having a lattice constant different from

that of the buffer layer;

a monocrystal insulating film formed on the strained-Si layer, the monocrystal insulating

film being made of a material having a rare earth structure with a lattice constant different from

that of Si; and

an electrode formed on the insulating film.

2. (Original) The semiconductor apparatus according to claim 1, wherein the electrode is

made of a crystalline semiconductor material.

3. (Original) The semiconductor apparatus according to claim 2, wherein the electrode is

made of crystalline SiGe.

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- 4. (Currently Amended) The semiconductor apparatus according to claim 1, wherein the insulating film is a monocrystal film including one selected from the group consisting of CeO₂, PrO₂, CaO₂, TbO₂, PrO₂, Dy₂O₃, Er₂O₃, Eu₂O₃, Gd₂O₃, Ho₂O₃, In₂O₃, Lu₂O₃, Lu₂O₃, Nd₂O₃, Pr₂O₃, Tb₂O₃, Tb₂O₃, Tl₂O₃, Tm₂O₃, Y₂O₃ and Yb₂O₃.
- 5. (Original) The semiconductor apparatus according to claim 1, wherein the buffer layer is made of monocrystal SiGe.
- 6. (Original) The semiconductor apparatus according to claim 1, wherein the buffer layer is formed on the silicon substrate through the insulating film.
 - 7. (Original) A semiconductor apparatus comprising:
 - a substrate:
- a buffer layer made of a monocrystal semiconductor material and formed on the substrate;
- a strained-silicon layer formed on the buffer layer and having a lattice constant different from that of the buffer layer;
- a source region and a drain region formed in the strained-silicon layer so as to be separated from each other;
- a gate insulating film formed on the strained-silicon layer sandwiched between the source region and the drain region and made of a monocrystal rare earth oxide having a lattice constant different from that of silicon; and
 - a gate electrode formed on the gate insulating film.

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- 8. (Original) A semiconductor apparatus according to claim 7, wherein the gate electrode is made of a crystalline semiconductor material.
- 9. (Original) The semiconductor apparatus according to claim 7, wherein the gate electrode is made of crystalline SiGe.
- 10. (Currently Amended) The semiconductor apparatus according to claim 7, wherein the gate insulating film is a monocrystal film including one selected from the group consisting of CeO₂, PrO₂, CaO₂, TbO₂, PrO₂, Dy₂O₃, Er₂O₃, Eu₂O₃, Gd₂O₃, Ho₂O₃, In₂O₃, Lu₂O₃, Lu₂O₃, Nd₂O₃, Pr₂O₃, Sm₂O₃, Tb₂O₃, Tl₂O₃, Tl₂O₃, Tm₂O₃, Y₂O₃ and Yb₂O₃.
- 11. (Original) The semiconductor apparatus according to claim 7, wherein the buffer layer is made of monocrystal SiGe.
- 12. (Original) The semiconductor apparatus according to claim 7, wherein the buffer layer is formed on the silicon substrate through the insulating film.
 - 13. 19. (Canceled)